

What is claimed is:

1. A graphics display method for continuously displaying graphics data on multiple display devices of a computer that contains a system memory directly accessed
5 by a CPU, the method comprising:

using a common clock source to synchronize blank periods of the display devices;

receiving a power saving signal from the CPU, said power saving signal indicates a request for executing a power saving process; and

10 executing the power saving process within the least common multiple occurrence of the blank periods of the display devices.

2. The method of claim 1 further comprising a step of detecting the upcoming least common multiple occurrence of the blank periods of the display devices before the
15 executing step.

3. The method of claim 1 wherein the blank period can be a horizontal blank period (HBP) or a vertical blank period (VBP).

20 4. The method of claim 3 wherein the horizontal blank period or the vertical blank period is provided by a graphics-processing unit.

5. A graphics display method for continuously displaying graphics data on multiple display devices of a computer that contains a system memory directly accessed

by a CPU, the method comprising:

using a common clock source to synchronize blank periods of the display devices;

receiving a power saving signal from the CPU, said power saving signal
5 indicates a request for executing a power saving process;

detecting an upcoming overlapping blank period of the display devices that is long enough for the power saving process to take place; and

executing the power saving process within the overlapping blank period of the display devices.

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6. The method of claim 5 wherein the blank period can be a horizontal blank period (HBP) or a vertical blank period (VBP).

7. The method of claim 6 wherein the horizontal blank period or the vertical
15 blank period is provided by a graphics-processing unit.

8. A graphics display method for continuously displaying graphics data on multiple display devices of a computer system that contains a system memory directly accessed by a CPU, the method comprising:

20 receiving a power saving signal from the CPU, said power saving signal indicates a request for executing a power saving process;

detecting an occurrence of the upcoming blank period for each display device;

marking the last blank period occurrence of the display devices as a reference blank period;

extending the blank periods of the other display devices to a time where all the display devices have an overlapping blank period longer than a power saving process period; and

executing the power saving process within the overlapping blank period.

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9. The method of claim 8 wherein the blank period can be a horizontal blank period (HBP) or a vertical blank period (VBP).

10. The method of claim 9 wherein the horizontal blank period or the vertical blank period is provided by a graphics-processing unit.

11. The method of claim 8 further comprising a step of obtaining a length of the power saving process period from the CPU before the step of extending.

15 12. A graphics display method for continuously displaying graphics data on multiple display devices of a computer system that contains a system memory directly accessed by a CPU, the method comprising:

receiving a power saving signal from the CPU, said power saving signal indicates a request for executing a power saving process;

20 detecting an occurrence of the upcoming blank period for each display device; marking the first blank period occurrence of the display devices as a reference blank period;

aligning the blank periods of the other display devices with the reference blank period so the display devices have an overlapping blank period longer than a power

saving process period; and

executing the power saving process within the overlapping blank period.

13. The method of claim 12 wherein the blank period can be a horizontal blank
5 period (HBP) or a vertical blank period (VBP).

14. The method of claim 13 wherein the horizontal blank period or the vertical
blank period is provided by a graphics-processing unit.

10 15. The method of claim 12 further comprising a step of obtaining a length of
the power saving process period from the CPU before the step of aligning.

16. The method of claim 12 wherein the step of aligning the blank periods of
the other display devices with the reference blank period is to force the blank periods of
15 the other display devices to occur at around the time the reference blank period takes
place.